भारतीय प्रौद्योगिकी संस्थान गांधीनगर

पालज, गांधीनगर, गुजरात 382 055 INDIAN INSTITUTE OF TECHNOLOGY GANDHINAGAR PALAJ, GANDHINAGAR, GUJARAT 382 055

Dr. Madhav K. Pathak

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Web: Low-Power Circuits & IoT Systems Lab

Advertisement for Postdoctoral Fellow Position Electrical Engineering, IIT Gandhinagar

Project: Research and Development of Power Management Circuits towards Electronic System Design of Battery-free Internet of Things (IoTs).

Required Qualification/Skills: PhD in Electrical or Electronics Engineering or related fields. Candidates who have submitted PhD thesis are also welcome to apply. Candidates with experience in design, analysis, and simulation of analog and/or power-electronics circuits are preferred. The candidate is expected to work towards publication in reputed journals/conferences.

Duration: Initially 12 months; extendable based on performance and availability of funds

Remuneration: INR 72,000/- per month (consolidated with no other allowances)

Application Procedure: Candidates must apply via Google Form:

https://forms.gle/icySZfYmPEJqHDYYA

Required documents for application to be uploaded via above Google Form: 1) CV listing educational degrees with percentage/CPI, internships, work experience, research skills, research publications, and any other relevant information; 2) Transcripts of coursework in UG/Masters/PhD combined as one PDF; 3) Proof of thesis submission/ thesis defense.

This is a rolling advertisement until the position is filled however applications received on or before 18th August 2024 (11:59 PM) will be considered on priority. The selection will be based on the online/offline interview and the shortlisted candidates will be intimidated in advance the date, time, and mode of the interview via email.

Contact: Please feel free to reach out to Prof. Madhav Pathak at madhav.pathak@iitgn.ac.in to discuss the project details or for any other queries.